Annex B



List of research specialisms for Sub-panel 11: Computer Sciences and Informatics

This REF topic classification is taken from the Association of Computing Machinery (ACM)
Computing Classification System March 2012 Revision, hence the use of American spelling.

Please have the REF topic classification number enclosed in '< >' as the first characters in the topic descriptor, followed by a text topic descriptor. If the output relates to more than one topic, please select the one to which the contribution is the greatest. Since the purpose is to assess whether we have sufficient expertise (and later, to assign outputs to sub-panel members), please use only a single topic. If there are many sub-topics which apply equally, then choose one at the highest level.

We think the ACM classification will apply to anything we have assessed in the past – from art to quantum physics – but we have allowed for 'Other topics'.

As noted, the information will enable the sub-panel to determine whether there are topics for which they do not have sufficient expertise, and a similar classification will be used to allocate outputs to sub-panel members for review. We realize this is a very coarse grained classification but a finer grained classification is impractical. This classification is only an aid to the sub-panel and will not affect assessment of outputs in any way.

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CM 2012 Topics	REF Classification
ardware	
Printed circuit boards	1
Electromagnetic interference and compatibility	1
PCB design and layout	1
Communication hardware, interfaces and storage	1
Signal processing systems	1
Digital signal processing	1
Beamforming	1
Noise reduction	1
Sensors and actuators	1
Buses and high-speed links	1
Displays and imagers	1
External storage	1
Networking hardware	1
Printers	1
Sensor applications and deployments	1
Sensor devices and platforms	
Sound-based input / output	1
Tactile and hand-based interfaces	1
Touch screens	1
Haptic devices	1
Scanners	1
Wireless devices	1
Wireless integrated network sensors	1
Electro-mechanical devices	1
Integrated circuits	1
3D integrated circuits	1
Interconnect	1
Input / output circuits	1
Metallic interconnect	1
Photonic and optical interconnect	1
Radio frequency and wireless interconnect	1
Semiconductor memory	1
Dynamic memory	1
Static memory	1
Non-volatile memory	1
Read-only memory	1
Digital switches	1
Transistors	1
Logic families	1
Logic circuits	1
Arithmetic and datapath circuits	1
Asynchronous circuits	1
Combinational circuits	1
Design modules and hierarchy	1
Finite state machines	1
Sequential circuits	1
Reconfigurable logic and FPGAs	1
Hardware accelerators	
	1
High-speed input / output Programmable logic elements	1

Programmable interconnect	1
Reconfigurable logic applications	1
Very large scale integration design	1
3D integrated circuits	1
Analog and mixed-signal circuits	1
Data conversion	1
Clock generation and timing	1
Analog and mixed-signal circuit optimization	1
Radio frequency and wireless circuits	1
Wireline communication	1
Analog and mixed-signal circuit synthesis	1
Application-specific VLSI designs	1
Application specific integrated circuits	1
Application specific instruction set processors	1
Application specific processors	1
Design reuse and communication-based design	1
Network on chip	1
System on a chip	1
Platform-based design	1
Hard and soft IP	1
Design rules	1
Economics of chip design and manufacturing	1
Full-custom circuits	1
VLSI design manufacturing considerations	1
On-chip resource management	1
On-chip sensors	1
Standard cell libraries	1
VLSI packaging	1
Die and wafer stacking	1
Input / output styles	1
Multi-chip modules	1
Package-level interconnect	1
VLSI system specification and constraints	1
Power and energy	1
Thermal issues	1
Temperature monitoring	1
Temperature simulation and estimation	1
Temperature control	1
Temperature optimization	1
Energy generation and storage	1
Batteries	1
Fuel-based energy	1
Renewable energy	1
Reusable energy storage	1
Energy distribution	1
Energy metering	1
Power conversion	1
Power conversion Power networks	1
Smart grid	1
Impact on the environment	1
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Power estimation and optimization	1
Switching devices power issues	•
Interconnect power issues	1
Circuits power issues	1
Chip-level power issues	1

Platform power issues	1
Enterprise level and data centers power issues	1
Electronic design automation	1
High-level and register-transfer level synthesis	1
Datapath optimization	1
Hardware-software codesign	1
Resource binding and sharing	1
Operations scheduling	1
Hardware description languages and compilation	1
Logic synthesis	1
Combinational synthesis	1
Circuit optimization	1
Sequential synthesis	1
Technology-mapping	1
Transistor-level synthesis	1
Modeling and parameter extraction	1
Physical design (EDA)	1
Clock-network synthesis	1
Packaging	1
Partitioning and floorplanning	1
Placement	1
Physical synthesis	1
Power grid design	1
Wire routing	1
Timing analysis	1
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Statistical timing analysis	1
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Design databases for EDA	1
Software tools for EDA	1
Hardware validation	1
Functional verification	1
Model checking	1
Coverage metrics	1
Equivalence checking	1
Semi-formal verification	1
Simulation and emulation	1
Transaction-level verification	1
Theorem proving and SAT solving	1
Assertion checking	1
Physical verification	1
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Design rule checking	1
Layout-versus-schematics	•
Power and thermal analysis	1
Timing analysis and sign-off	1
Post-manufacture validation and debug	1
Bug detection, localization and diagnosis	1
Bug fixing (hardware)	1
Design for debug	1
Hardware test	1

Analog, mixed-signal and radio frequency test	1
Board- and system-level test	1
Defect-based test	1
Design for testability	1
Built-in self-test	1
Online test and diagnostics	1
Test data compression	1
Fault models and test metrics	1
Memory test and repair	1
Hardware reliability screening	1
Test-pattern generation and fault simulation	1
Testing with distributed and parallel systems	1
Robustness	1
Fault tolerance	1
Error detection and error correction	1
Failure prediction	1
Failure recovery, maintenance and self-repair	1
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System-level fault tolerance	1
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Yield and cost optimization	1
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Circuit hardening	1
Early-life failures and infant mortality	1
Process, voltage and temperature variations	1
Signal integrity and noise analysis	1
Transient errors and upsets	1
Safety critical systems	1
Emerging technologies	1
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Emerging architectures	1
Emerging languages and compilers	1
Emerging simulation	1
Emerging tools and methodologies	1
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Bio-embedded electronics	1
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Carbon based electronics	1
Cellular neural networks	1
Flexible and printable circuits	1
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Microelectromechanical systems	1
Nanoelectromechanical systems	1
Emerging interfaces Memory and dense storage	
Memory and dense storage	1
Emerging optical and photonic technologies	1
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Plasmonics	1

Quantum technologies	1
Single electron devices	1
Tunneling devices	1
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